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PATENT
(5500-88800/TT5375)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/676,437

Filed: October 1, 2003

Inventor(s):

Mitchell Alsup

Gregory William Smaus

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Michael A. Fillippo

Benjamin T. Sander

Title: System And Method For
Handling Exceptional
Instructions In A Trace
Cache Based Processor

§ Examiner: Kim, Hong Chong

§ Group/Art Unit: 2185

§ Atty. Dkt. No: 5500-88800

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Mail Stop Missing Parts, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Robert C. Kowert

Printed Name



Mar. 2, 2006

Signature

Date

REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

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P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The official Filing Receipt received by our office lists the Atty. Docket No. as 5500-91600. However, the correct Atty. Docket No. is 5500-88800. I have provided a copy of the Filing Receipt with the change noted thereon. Applicants respectfully request that this change be made and a corrected Filing Receipt be reissued in the above-identified application.

Respectfully submitted,



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Date: Mar. 2, 2006



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FILING RECEIPT



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Date Mailed: 12/23/2003

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Assignment For Published Patent Application

Advanced Micro Devices, Inc., Sunnyvale, CA;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 12/23/2003

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Non-Publication Request: Yes

Early Publication Request: No

cc: IAMD 1-8-04

Title

System and method for handling exceptional instructions in a trace cache based processor

Preliminary Class

711

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